

Docket No.: 5016-0103PUS1
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Tadahiro OHMI et al.

Application No.: 10/581,035

Confirmation No.: 5997

Filed: April 20, 2007

Art Unit: 2871

For: THIN FILM TRANSISTOR INTEGRATED
CIRCUIT DEVICE, ACTIVE MATRIX
DISPLAY DEVICE, AND MANUFACTURING
METHODS OF THE SAME

Examiner: M. H. Caley

REPLY TO RESTRICTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In reply to the Restriction Requirement dated November 19, 2009, the following remarks are respectfully submitted in connection with the above-identified application.

This reply includes: Remarks